Faculty Curriculum Vitae

Name of faculty: Trimukhe Kumar Mohanrao

Present Position: Lecturer in Electronics Engg.

Address: Puranmal Lahoti Govt. Polytechnic, Nandi Stop, Ausa Road, Latur, Pin code: 413512

Mobile no: 7218802789

Email ID: kumarplgpl@gmail.com

Date of Birth: 22nd Nov.1990

Qualification:

Sr. No	Examinition	Institute Name	Board/University	% of marks	Class award
1	SSC	Model English School, Majalgaon	Maharashtra State Board	76.26%	First Class with Distinction
2.	HSC	Majalgaon Science College, Majalgaon	Maharashtra State Board	71.83%	First Class
3.	B.E.(E&TC)	Government College Of Engineering Aurangabad	Dr. BAMU University, Aurangabad	76.25%	First Class with Distinction
4.	M.E.(E&TC)	College Of Engineering, Pune.	Savitribai Phule University, Pune	86.60%	First with Distinction



Career details:

Sr.No	Organization	Designation	Duration	Period	Work done in Brief
1	Infosys	Software	22/12/2015	1 Year 1	Application
	Technolgies,	Engineer	to	Month	Development
	Ltd. Mysore		25/01/2017		(Dot Net, Python)
2	P.L.G.P. Latur	Lecturer in	30/01/2017	4 Year 4	Teaching, Works
		Electronics	to	months	allotted by Head
			Till date	18 days	_

Specialized Training courses attended:

Sr.no	Course/Training title	Training period	Training Place	Training Sponsored Agency
1	Induction raining	29/05/2017	MET's BKC,	NITTTR,
	Programme Phase-I	to	Institute of	Bhopal
		09/06/2017	Technology -	
			Polytechnic, Nashik	
2	Hand on practice on PLC,	11/12/2017	Government	MSBTE
	Pneumatic & Electro-	to	Polytechnic, Hingoli	
	Pneumatic and its	15/12/2017		
	application in industrial			
	automation & control			
3	Process of NBA	04/06/2018	A.G.Patil	AICTE and
	Accreditation	to	Polytechnic	ISTE
		09/06/2018	Institute, Solapur	
4	Tools for Scientific	12/11/2018	P.L. Government	NITTTR,
	Research in Engineering &	to	Polytechnic, Latur	Chandigarh
	Science through ICT	16/11/2018		
5	Next Generation Wireless	26/11/2018	P.L. Government	NITTTR,
	Technologies through ICT	to	Polytechnic, Latur	Chandigarh
		30/11/2018		

6	Open Source Technology	04/02/2019		DTE
		to	Polytechnic, Latur	
		08/02/2019		
7	Curriculum	25/02/2019	P.L. Government	NITTTR,
	Implementation through	to	Polytechnic, Latur	Chandigarh
	ICT	01/03/2019		
8	Induction raining	20/05/2019	P.L. Government	NITTTR,
	Programme Phase-II	to	Polytechnic, Latur	Bhopal
		31/05/2019		1
9	NBA Accreditation	19/08/2019	P.L. Government	NITTTR,
		to	Polytechnic, Latur	Bhopal
		23/08/2019	5	1
10	Problem solving through	27/01/2020	Swayam	NPTEL,
10	Programming In C	to		AICTE
		17/04/2020		
11	Student Evaluation (Module	03/02/2020	P.L. Government	NITTTR,
11	III:IPD)	to	Polytechnic, Latur	Chandigarh
		07/02/2020		Chanaigun
12	Module 1 Orientation	02/03/2020	National Institute of	NITTTR ,
	Towards Technical	to	Technical Teachers	Chennai
	Education and Curriculum	27/04/2020	Training (Swayam)	
	Aspects			
13	Module 2 Professional	02/03/2020	National Institute of	NITTTR,
	Ethics & Sustainability	to	Technical Teachers	Bhopal
		27/04/2020	Training (Swayam)	
14	Module 5_ Technology	02/03/2020	National Institute of	NITTTR
	Enabled Learning & Life	to	Technical Teachers	,Chennai
	Long Self Learning	27/04/2020	Training (Swayam)	
15	Module 7_ Creative Problem	02/03/2020	National Institute of	NITTTR,
	Solving, Innovation and	to	Technical Teachers	Chandigarh
	Meaningful R & D	30/04/2020	Training (Swayam)	
16	Module 3_ Communication	01/04/2021	National Institute of	NITTTR ,
	Skills, Modes & Knowledge	to	Technical Teachers	Chandigarh
	Dissemination	31/05/2021	Training (Swayam)	
17	Module 4_Instructional	01/04/2021	National Institute of	NITTTR,
	Planning and Delivery	to	Technical Teachers	Bhopal
		31/05/2021	Training (Swayam)	
18	Module 6_ Student	01/04/2021	National Institute of	NITTTR,
	Assessment and Evaluation	to	Technical Teachers	Chennai
		16/06/2021	Training (Swayam)	

19	Module 8_ Institutional	01/04/2021	National Institute of	NITTTR,
	Management &	to	Technical Teachers	Chandigarh
	Administrative Procedures	31/05/2021	Training (Swayam)	_
20	Industrial Training in	23/08/2021	P.L. Government	NIELIT,
	Robotics Process	to	Polytechnic,	Aurangabad
	Automation	03/09/2021	Latur(online)	
21	Cyber Security and Forensics	21/02/2022	P.L. Government	DTE
		to	Polytechnic,	
		25/02/2022	Latur(online)	
22	Industrial Training in	02/02/2022	P.L. Government	NIELIT,
	Additive Manufacturing and	to	Polytechnic,	Calicut
	its Applications	15/02/2022	Latur(online)	

Research Project:

Sr.No	Title of Project	Year
1.	Design and Implementation of Antenna system for Very	2014-2015
	Low Power Transmitter (VLPT)	

Paper Publication:

[1] "Design of Substantial Antenna System for Very Low Power Transmitter",11th July 2015 National Conference on Signal & Data Processing (NCSDP-2015).

Signature

Kumar Mohanrao Trimukhe