Faculty CV

Name of faculty: Smt. Renuka Vijaykumar Mahagaonkar

Present Position: Lecturer in Electronics Engg.

Address: Puranmal Lahoti Government Polytechnic, Nandi stop,

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Mobile no: 9422023737

Email ID: renuka2209@gmail.com

Date of Birth: 22th September 1976

Qualification:

Sr. No	Examination	Institute Name	Board/University	% of marks	Class award
1	SSC	Sri.Saraswati Shishu Mandir, Nirmal, Dist. Adilabad, Telangana	Secondary Board, Andhra Pradesh, Hyderabad.	87.17%	First class with Distinction
2.	HSC	Haricharan College, Nizamabad	Board Of Intermediate Education, Andhra Pradesh, Hyderabad.	74.80%	First class with Distinction

3.	B.E. (Electronics)	M.S.Bidve Engineering College Latur	Dr.B.A.M.U. Aurangabad	60.00%	First class
4.	M.E. (Electronics)	S.G.G.S NANDED	S.R.T.M.U. Nanded	64.41%	First class
5	P.hD. (Electronics Engineering)	Deogiri Institute of Engineering and Management Studies, Aurangabad.	Dr. B.A.M.U. Aurangabad.	Perceiving	

Career details:

Sr.No	Organization	Designation	Duration	Period	Work done in Brief
1	V.R.E.C. Nizamabad. J.N.T.U. Andhrapradesh	Lecturer	01/09/1998 to 31/03/2001	2 years 7months	As per designation
2	G.R.W.P. Latur	Lecturer	22/01/2006 to 11/01/2008	2 years	As per designation

3	M.S.Bidve Engineering College, Latur.	Lecturer	15/01/2008 to 20/10/2010	2 years 09 months	As per designation
4	G.R.W.P. Latur	Lecturer	21/10/2010 to 10/08/2021	10 years 9 months	As per designation
5	P.L.G.P. Latur	Lecturer	11/08/2021 to till date		As per designation

Specialized Training courses attended:

Sr. No	Course/Training title	Training period	Training Place	Training Sponsored Agency
1	Telematics & Mobile Communication	25/08/2011 - 29/08/2011 (One Week)	Sou. Venutai Chavan Polytechnic, Pune.	MSBTE
2	Trends in VLSI design & Embedded System	23/01/2012 - 27/01/2012 (One Week)	M.S.Bidve Engineering College, Latur.	ISTE
3	Positive relations & Self motivation attitude	24/09/2012 - 28/09/2012	NITTR Extension centre, Pune.	NITTTR Bhopal

	development & Time management	(One Week)		
4	Induction training Programme Phase - 1	03/06/2013 - 14/06/2013 (Two Weeks)	Brahmdevdada Mane Polytechnic Belati, Solapur.	NITTTR Bhopal
5	VLSI Design	19/05/2014 - 23/05/2014 (One Week)	NITTR Extension centre, Pune.	NITTTR Bhopal
6	Nature Inspired Computations in Engineering	18/05/2015 - 22/05/2015 (One Week)	Dr.BATU, Lonere, Dist. Raigad.	TEQIP-II
7	Recent trends in non-conventional Energy with the focus on Solar and Wind Energy	01/06/2015- 13/06/2015 (One Week)	Government College Of Engineering, Karad.	TEQIP-II
8	Soft Computing	01/06/2015- 13/06/2015 (Two Weeks)	Sinhgad College Of Engineering, Pune.	AICTE
9	Refresher's Training Program	05/10/2015 - 09/10/2015 (One Week)	YASHADA, Pune.	YASHADA
10	CMOS, Mixed signal And Radio frequency VLSI Design	30/01/2017- 04/02/2017 (Two Weeks)	M.S.Bidve Engineering College, Latur.	ISTE

11	Induction training Programme Phase - II	22/05/2017 - 02/06/2017 (Two Weeks)	Shreeyash Polytechnic, Aurangabad.	NITTTR Bhopal
12	Tools for Online Teaching Learning Process	20/04/2020 - 17/05/2020 (Four Weeks)	Government Polytechnic, Jalgaon.	MSBTE

Research Project:

Sr. No.	Course/Degree	Title of Project	Year
1	ME	Wavelet Based Digital Image Watermarking	2003 -2005

Paper Publication:

1. R.V.Mahagaonkar, "Digital Image Watermarking Algorithm", ISSN (Print): 2278-8948, Volume-4, Issue-5,6, Page no.69-73, 2015.

2. R.V.Mahagaonkar, "Digital Image Watermarking by Optimization of Weight Factor (α)", ISSN (Online): 2349-9338,Volume -3, Issue-4, Page no. 19-22, 2016.

Signature

Mrs. R.V.Mahagaonkar